

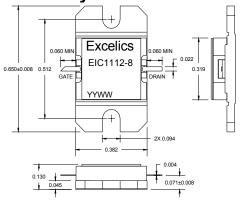


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# 11.7-12.7 GHz 8-Watt Internally Matched Power FET

### **FEATURES**

- 11.7- 12.7GHz Bandwidth
- Input/Output Impedance Matched to 50 Ohms
- +39.5 dBm Output Power at 1dB Compression
- 6.0dB Power Gain at 1dB Compression
- 30% Power Added Efficiency
- Hermetic Metal Flange Package





Caution! ESD sensitive device.

## **ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C)**

SYMBOL	PARAMETERS/TEST CONDITIONS <sup>1</sup>		TYP	MAX	UNITS
P <sub>1dB</sub>	Output Power at 1dB Compression $f = 11.7-12.7GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200\text{mA}$	38.5	39.5		dBm
G <sub>1dB</sub>	Gain at 1dB Compression $f = 11.7-12.7GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200\text{mA}$	5	6		dB
ΔG	Gain Flatness $f = 11.7-12.7 GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200 \text{mA}$			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression V <sub>DS</sub> = 10 V, I <sub>DSQ</sub> ≈ 2200mA		30		%
Id <sub>1dB</sub>	Drain Current at 1dB Compression f = 11.7-12.7GHz		2300	2800	mA
IM3	Output 3rd Order Intermodulation Distortion Δf=10MHz 2-Tone Test. Pout=28.5 dBm S.C.L Vds = 10 V, I <sub>DSQ</sub> ≈ 65% I <sub>DSS</sub> f = 12.7GHz		-43		dBc
I <sub>DSS</sub>	Saturated Drain Current $V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}$		4000	5000	mA
$V_P$	Pinch-off Voltage $V_{DS} = 3 \text{ V}, I_{DS} = 40 \text{ mA}$		-2.5	-4.0	V
R <sub>TH</sub>	Thermal Resistance <sup>3</sup>		35	4	°C/W

### Note: 1) Tested with 100 Ohm gate resistor.

### **ABSOLUTE MAXIMUM RATING**<sup>1,2</sup>

SYMBOLS	PARAMETERS	ABSOLUTE <sup>1</sup>	CONTINUOUS <sup>2</sup>
Vds	Drain-Source Voltage	15	10V
Vgs	Gate-Source Voltage	-5	-4.5V
lgsf	Forward Gate Current	86.4mA	28.8mA
lgsr	Reserve Gate Current	-14.4mA	-4.8mA
Pin	Input Power	38.5dBm	@ 3dB Compression
Tch	Channel Temperature	175 °C	175°C
Tstg	Storage Temperature	-65 to +175 °C	-65 to +175 °C
Pt	Total Power Dissipation	38W	38W

Note: 1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.

<sup>2)</sup> S.C.L. = Single Carrier Level.

<sup>3)</sup> Overall Rth depends on case mounting.



## **EIC1112-8**

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